



JEPPIAAR
ENGINEERING COLLEGE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Innovations in Teaching and Learning

EC8392-DIGITAL ELECTRONICS

Project Title: 7-Segment Display Counter

Academic Year: 2021-22

Semester/Year: III/II

Type: MINI PROJECT

Objectives

To design and implement a digital system that **counts from 0 to 9** and **displays the output on a 7-segment display** by using **combinational and sequential logic circuits**, thereby understanding the **integration of counters and display decoders** in real-time applications.

Outcomes

- Experience **in writing technical documentation and giving demonstrations**, boosting communication and presentation abilities.
- Team **collaboration and project planning skills**, developed through teamwork during mini project execution.