

FACULTY PUBLICATIONS

Academic Year: 2016- 2017

PAPER PUBLICATIONS (2016-2017)			
INTERNATIONAL CONFERENCE			
SL. No	AUTHORS	TITLE OF THE PAPER	CONFERENCE DETAILS
1.	Dr. J. Jebastine, et al.	Design of 5:2 and 15:4 Full Adder Based VLSI Compressors using TSPICE	ICONSTEM 2017
2.	Dr. J. Venu Gopala Krishnan, et al.	DWT-Based Digital Image Watermarking Effects on Color Spaces using Arnold Transform	ICONSTEM 2017
3.	Mr. S. Ranjith et al.	Design of High Frequency PLL using H-Spice	ICONSTEM 2017
4.	Dr. J. Venu Gopala Krishnan, et al.	Design of LNA Model for Impedance Matching Networks using MATLAB	ICONSTEM 2017
5.	Dr. J. Venu Gopala Krishnan, et al.	Efficient Transformation of Highly Secured Data Communication using DWT-SVD	ICONSTEM 2017
6.	Dr. J. Jebastine, et al.	Low Power VLSI Implementation of Analog Dual AGC Circuit using Wallace Tree Multiplier	ICONSTEM 2017
7.	Mr. T. Jagadesh, et al.	A Low Power FIR Filter Architecture using Null Convention Logic	ICONSTEM 2017
8.	Ms. P. Jeya Priyanka, et al.	Implementation of Power Efficient Flip Flop for Low Power Clocking System	ICONSTEM 2017
9.	Mr. J. Ranjith Kumar, et al.	Reduction of Complexity for Fading Channels by using Adaptive Lattice Reduction Method and LLL Algorithm	ICONSTEM 2017
10.	Ms. T. Vishnu Priya, et al.	Design of Low Power Adiabatic based SRAM Cells using Nano-devices for Memory Array	ICONSTEM 2017
11.	Mr. B. Vinu Vignesh, et al.	An FPGA Implementation of High-Level Secured Data Transmission through Cryptography Steganography using VLSI	ICONSTEM 2017
12.	Mr. S. Ramesh, et al.	High Performance Energy Efficient Transpose Form FIR Filter for Fixed and Reconfigurable Applications	ICONSTEM 2017
13.	Mr. T. R. Chenthil, et al.	Wireless Sensor Network Based Border Surveillance using Multiple Signatures	ICONSTEM 2017
14.	Mr. G. C. Jagan, et al.	Survey of Improving Energy Efficiency in Wireless Communication	ICONSTEM 2017
15.	Mr. S. Ramesh, et al.	The Improved LS Channel Estimation Method using DFT-Based Algorithm For MIMO-OFDM Transceiver	ICONSTEM 2017
16.	Dr. J. Venu Gopala Krishnan, et al.	Design and Controlling of Power Optimization Based SQRT Carry Select Adder	ICONSTEM 2017

17.	Dr. J. Jebastine, et al.	Design of High Performance of CMPT Flip-Flop using Power Gating Technology	ICONSTEM 2017
18.	Mrs. V. Nanammal, et al.	Design of On- Chip Light Weight Sensors for Detection of Counterfeiting IC	ICONSTEM 2017
19.	Mr. T. R. Chenthil, et al.	Stage Wise Approach for Energy Efficient Data Transmission in Wireless Sensor Networks	ICONSTEM 2017
20.	Mrs. C. Anitha, et al.	LDPC coded High Capacity Optical CDMA System using OOK to 32 QAM Wavelength Converters	ICONSTEM 2017
21.	Mrs. R. Bhavani, et al.	A Novel PCA and Wavelet Based Denoising of Biomedical Images	ICONSTEM 2017
22.	Mr. T. Jagadesh, et al.	FPGA Implementation of Adaptive Beam Forming using 3 Radial Basis Function Neural N/Ws	ICONSTEM 2017
23.	Dr. J. Jebastine, et al.	Real Time Implementation of Smart Trash using IOT	ICONSTEM 2017
24.	Mrs. R. Jhansi Rani, et al.	Detection and Quantification of Lesions Caused by Diabetic Retinopathy	ICONSTEM 2017
25.	Dr. J. Jebastine, et al.	Implementation of Tillage system (STS) using Wireless Technology	ICONSTEM 2017
26.	Mr. Arun Vijayakumar, et al.	5-10GB / S Burst Mode Clock and Data Recovery Circuit Based on CMOS with Asynchronous PI for PONS: Theoretical & Experimental	ICONSTEM 2017
27.	Ms. Madona B Sahaai et al.	High Speed Energy Efficient Cell-Based Adder for VLSI Design	ICONSTEM 2017
28.	Mr. G. Balachandran, et al.	Video Stabilization using Block-Based Triangulation without Accumulative Global Motion Estimation	ICONSTEM 2017
29.	Mrs.V. Nanammal, et al.	Low Power and Area Efficient Design of Full Adder Circuit using LCPMOS technique	ICONSTEM 2017
30.	Mr. S.Ranjith, et al.	Design of VLSI based BMCDR for High-Speed Wireless Networks using 32nm FINFET Technology	ICONSTEM 2017
31.	Mrs. P. Mahalakshmi, et al.	Home Diagnostic Pulmonary Function Test using Signal Processing	ICONSTEM 2017
32.	Mrs. F. Dalphin Mary, et al.	Analysis and Reduction of Power in CMOS SDML with Power Gating Of 4:2 Compressor Using Lector with DML	ICONSTEM 2017

INTERNATIONAL JOURNALS (2016-2017)

SL. No	AUTHORS	TITLE OF THE PAPER	JOURNAL DETAILS
1.	Mr. S. Ranjith, et al.	A Novel Design & Analysis of Low Power DTC in TDC for PLL Based Applications using FINFET FET & GNFET in 16nm Technology	International Journal of Control Theory and Applications Vol.9. Sp-34/July 2016. (Scopus Indexed Journal)
2.	Mr. T. Jagadesh,, et al.	FPGA Implementation of Brain Tumor Detection using Lifting Based DWT Architectures	International Journal of Control Theory and Applications Vol.9. Sp-34/ July 2016. (Scopus Indexed Journal)
3.	Mr. T. Jagadesh, et al.	ECG Decomposition using Wavelet Transforms – A Review	Middle East Journal of Scientific Research Vol.25. Issue 1/ April 2017. (Scopus Indexed Journal)
4.	Mr. T. Jagadesh, et al	Muscular Artifacts Removal in Electrocardiogram by Combining Discrete Wavelet Transform and Adaptive Noise Cancellation	Research Journal of Applied Sciences, Engineering and Technology Vol.13. Issue 10/August 2016. (Scopus Indexed Journal)
5.	Mr. S. Ranjith, et al.	Analysis of Leakage Power Reduction in Low Power Bio Instrumentation Amplifier	World Applied Sciences Journal Sp(34). Issue 9/ September 2016. (Scopus Indexed Journal)
6.	Mrs. N. Kiranmayee, et al.	A Novel Fuzzy C-Means Combined Fuzzy-KNN Methodology for Detecting and Classifying MRI Brain Tumor	International Journal of Control Theory and Applications Vol.6. Issue 9/February 2017. (Scopus Indexed Journal)
7.	Dr. J. Venu Gopala Krishnan, et al.	Detection of IC using AF Based Sensor	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2391, Oct – Dec 2016
8.	Mr. G. Balachandran, et al.	Design of Low Power Multipliers with Braun Architecture using Column by Passing Multipliers	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2394, Oct – Dec 2016
9.	Mr. T. Jagadesh, et al.	Multiple Bit Error Detecting and Correcting Parallel Decoder for BCH	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2398, Oct – Dec 2016

SL. No	AUTHORS	TITLE OF THE PAPER	JOURNAL DETAILS
10.	Dr. J. Jebastine, et al.	Design of CMPFFE in Current Mode Signaling for High Performance and Low Power	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2401, Oct – Dec 2016
11.	Mr. S. Ranjith, et al.	Effective Power Consumption Encoding Technique for DSRC application	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2405, Oct – Dec 2016
12.	Dr. J. Venu Gopala Krishnan, et al.	A Comparative Study on CBL, COT, BEC Techniques Based Carry Select Adder	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2409, Oct – Dec 2016
13.	Mrs. V. Nanammal, et al.	A Literature Review Hybrid Watermarking Based VLSI Schemes for Real Time Authentication	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2412, Oct – Dec 2016
14.	Dr. J. Jebastine, et al.	Implementation of Pass Transistor Logic in Recursive Parallel Self – Timed Adder to Achieve Low Power	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2420, Oct – Dec 2016
15.	Dr. J. Venu Gopala Krishnan, et al.	Electronic Waste Management	ISSN-0974-2115, JCPS, Volume -9, Issue 4, Pg. 2423, Oct – Dec 2016