

FACULTY PUBLICATIONS

Academic Year: 2015- 2016

PAPER PUBLICATIONS (2015-2016)			
INTERNATIONAL CONFERENCE			
SL. No	AUTHORS	TITLE OF THE PAPER	CONFERENCE DETAILS
1.	Mr. S. Ranjith, et al.	A Novel Design and Analysis of Reversible Parallel Shifter using New FF-Gate	ICONSTEM 2016
2.	Dr. J. Jebastine, et al.	Efficient Identification of Similar Videos using Grid Based Encoding and Linear Chaining Technique	ICONSTEM 2016
3.	Dr. J. Jebastine, et al.	Design and Estimation of High-Speed Adders for Ultra Power and Energy Circuits using SAL	ICONSTEM 2016
4.	Mrs. P. Mahalakshmi, et al.	ARM Microcontroller-Based Vehicle Monitoring and Controlling using CAN protocol	ICONSTEM 2016
5.	Dr. J. Jebastine, et al.	Implementation of 4:2 Compressor using Lector A&B for Ultra-Low Power Applications	ICONSTEM 2016
6.	Dr. J. Jebastine, et al.	Design of Recursive Parallel Self-Timed Adder using Transistor Logic to Achieve Low Power	ICONSTEM 2016
7.	Dr. J. Jebastine, et al.	Design of FAM using Structured Modified Booth Recoding Technique for DSP Applications	ICONSTEM 2016
8.	Dr. J. Jebastine, et al.	Design of CMPFFE in Current Mode Signaling for High Performance and Low Power	ICONSTEM 2016
9.	Dr. J. Venu Gopala Krishnan, et al.	Fault Tolerant Parallel Filter	ICONSTEM 2016
10.	Dr. J. Venu Gopala Krishnan, et al.	Detection of IC Usage using AF-Based Sensor	ICONSTEM 2016
11.	Dr. J. Venu Gopala Krishnan, et al.	BIST Architecture for Fault Detection and Diagnosis for Interconnect Resources and CLB in S-RAM Based FPGA	ICONSTEM 2016
12.	Dr. J. Venu Gopala Krishnan, et al.	A Comparative Study on CBL, COT, BEC Techniques-Based Carry Select Adder	ICONSTEM 2016

13.	Mr. T. Jagadesh, et al.	Multiple Bit Error Detecting and Correcting Parallel Decoder for BCH codes	ICONSTEM 2016
14.	Mr. T. R. Chenthil, et al.	A Comprehensive Review of Energy Efficiency in Underwater Sensor Networks	ICONSTEM 2016
15.	Mr. S. Ranjith, et al.	Efficient Power Consumption Encoding Technique for DSRC Applications	ICONSTEM 2016
16.	Mr. S. Ranjith, et al.	Generation of Multibit using Adders for Power Optimization	ICONSTEM 2016
17.	Dr. J. Jebastine, et al.	A GPRS Based Wireless Wearable Electronic Sensor using Flexible Printed Sensor Array	ICONSTEM 2016
18.	Mr. S. Ranjith, et al.	Invariant Image Watermarking using NLM filter	ICONSTEM 2016
19.	Dr. J. Venu Gopala Krishnan, et al.	Automatic Image Registration using Image Transformations	ICONSTEM 2016
20.	Dr. J. Jebastine, et al.	Transmission Efficient Modeling to Method using Decentralized Cluster Head by Adaptive Gossip Algorithm	ICONSTEM 2016
21.	Mr. M. Sinuvasan, et al.	Design of a $\pi/4$ DQPSK Modem using CORDIC Algorithm	ICONSTEM 2016
22.	Mr. T. Jagadesh, et al.	Thyroid Segmentation and Volume Estimation in Ultrasound Images	ICONSTEM 2016
23.	Mr. T. Jagadesh, et al.	Multi Sensor Railway Security System	ICONSTEM 2016
24.	Dr. J. Jebastine, et al.	Implementing and Reducing of Noise in Currency System using CLAE Technique	ICONSTEM 2016
25.	Mr. S. Ranjith, et al.	Analysis of Stereo Video Compression Algorithm using DIBR.	ICONSTEM 2016
26.	Dr. S. K. Padmanabhan, et al.	Bit Error Rate Reduction in Noisy and Multipath Channels for Cognitive Radio by Root Insertion Technique in OFDM Transceivers	ICONSTEM 2016
27.	Mrs. R. Bhavani, et al	Detection of Trapped or Buried Victims in Debris using Ground Penetrating Radars	ICONSTEM 2016
28.	Mr. G. Balachandran, et al.	Design of Low power Multipliers with Braun Architecture using Column Bypassing Multipliers	ICONSTEM 2016

29.	Mr. G. Balachandran, et al.	Video Stabilization using Block Based Triangulation Without Accumulative Global Motion Estimation	ICONSTEM 2016
30.	Mr. M. Suman, et al.	Biometric Fingerprint Authentication with Minutiae using Ridge Feature Extraction	ICONSTEM 2016
31.	Mr. T. R. Chenthil, et al.	Secured Energy Efficient Based Data Transmission in Wireless Sensor Networks Using a Stage Wise Approach	ICONSTEM 2016
32.	Dr. J. Jebastine, et al.	Blood Vessel Detection in Retina using MATLAB	ICIETET 2016
33.	Dr. J. Venu Gopala Krishnan, et al.	Survey on Grapheme Field Effect Transistor (GNR-FET)	ICIETET 2016
34.	Dr. J. Jebastine, et al.	Design and Implementation of Low Power Multiplexer using H-Spice	ICIETET 2016
35.	Mr. S. Ranjith, et al.	Design of D-Filp Flop using Hybrid AVL Techniques for Power Optimization	ICIETET 2016
36.	Mrs. P. Mahalakshmi, et al.	Low power Multiplexer Design with Domino Logic using H-spice	ICIETET 2016
37.	Mr. T. Jagadesh, et al.	Design and Analysis of SRAM Cell 45nm and 180nm Technology	ICIETET 2016
38.	Mr. S. Ranjith, et al.	Lower Power 4:2 Data Compressor using XOR Implementation	ICIETET 2016
39.	Dr. J. Jebastine, et al.	Design and Reduction of Power in High Performance Master Slave JK Flip-Flop using GDI Techniques in H-Spice	ICIETET 2016
40.	Mr. T. Jagadesh, et al.	A Novel Speckle Noise Reduction in Bio-Medical Images using PCA and Wavelet Transform	IEEE WISPNET 2016

INTERNATIONAL JOURNALS (2015-2016)

SL. No	AUTHORS	TITLE OF THE PAPER	JOURNAL DETAILS
1	Mrs. R. Jhansi rani, et al.	A New Defected Ground Structure for Proximity Coupled Antenna	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)

2	Mr. T. R. Chenthil, et al.	Mac-Layer Configurations Based Improving Energy Efficiency for Wireless Sensor Network	Australian Journal of Basic and Applied Science Vol.9. Issue. 20 / June 2015. (Scopus Indexed Journal)
3	Mr. S. Ranjith, et al.	Fault Tolerant Reconfigurable Shift Register Architecture using CNTFET	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
4	Mr. S. K. Binu Siva Singh, et al.	A Survey on Methodology for Li-Fi(Light Fidelity) Technology	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
5	Mr. G. Balachandran, et al.	Modified Sample and Hold Circuit for SAR-ADC	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
6	Mrs. B.V. Kavetha, et al.	Decide, Detect and Classify Benign and Malignant in Mammograms using CV-Partitioning Method	ARPN Journal of Engineering and Applied Sciences Vol.10. Issue. 10/ June 2015. (Scopus Indexed Journal)
7	Mrs. V. Nanammal, et al.	Efficient Low Power Digital Designs and Analysis using Power Gating Techniques	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
8	Mrs. V. Nanammal, et al.	A Probabilistic Framework for Collision Avoidance in Automobiles using GSM	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
9	Dr. J. Jebastine, et al.	Region Based Segmentation and Analysis of Carpal Bones Using Fuzzy C	Journal of Chemical and Pharmaceutical Sciences Vol.8. Issue. 2/ June 2015. (Scopus Indexed Journal)
10	Dr. J. Jebastine, et al.	Architecture Design of Low Power Multiplier for Signal Processing Applications	International Journal of Applied Engineering Research Vol.10. Issue. 22/ June 2015. (Scopus Indexed Journal)

11.	Mrs. V. Nanammal, et al.	VLSI Based Design of an Efficient Hybrid Watermarking Scheme for Multimedia Content Protection	Indian Journal of science and technology Vol.8. Issue 19/ August 2015. (Scopus Indexed Journal)
12.	Mr. S. Ranjith, et al.	Energy Efficient Multiplexer and Demultiplexer using FINFET Technology	Research journal of Applied sciences, Engineering and Tech. Vol.10. Issue. 8/ June 2015. (Scopus Indexed Journal)
13.	Dr. J. Jebastine, et al.	A Real Time Elevator in Metro Rail Stations	International Journal of Advanced Research Trends in Engineering and Technology Vol.3. Sp(3)/ April 2016. (Scopus Indexed Journal)
14.	Mr. T. Jagadesh, et al.	A Novel Pipelined Adaptive RLS Filter for ECG Noise Cancellation	Research journal of Applied sciences, Engineering and Tech. Vol.11. Issue. 5/ June 2015. (Scopus Indexed Journal)
15.	Mr. T. Jagadesh, et al.	A Survey on Electronic Counter Measure Techniques for RADAR Signal Processing Applications	Australian Journal of Basic and Applied Science Vol.9. Issue. 22/ July 2015. (Scopus Indexed Journal)