



STAFF DETAILS

Staff Name	:	Mr.T.Jagadesh
Faculty ID	:	10462
Date of Joining	:	04.09.2014
Designation	:	Assistant Professor
Qualification	:	M.Tech. (Ph.D)
Teaching Experience	:	4 years 6 months
Area of specialization	:	VLSI Design,
Subjects handled	:	Electronic Circuits II, VLSI Signal processing, Transmission lines and wave guides, Advanced computer architecture, Linear Integrated circuits.
Books published	:	
Journals published	:	3
Conference / workshop attended	:	21
Patent details	:	3
Funded Project Details	:	
Awards & Achievements	:	100% result for 6 times